

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM.	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10085889	02/28/2002	257	254	2815	Dram

*APPLICANTS: Rendon Michael; Taylor William; Sing David;
2815

*CONTINUING DATA VERIFIED:

* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
Verified and Acknowledged Examiner's initials		
TITLE : Method of forming a semiconductor device having an energy absorbing layer and structure thereof		
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Cl. 000	Print Cl. 000
Assistant Examiner		000	000
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Check Dwg.	File Dwg.
TERMINAL		Print Fig.	
DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH: DISK (CRF) CD-ROM
(Attached in pocket on right inside flap)